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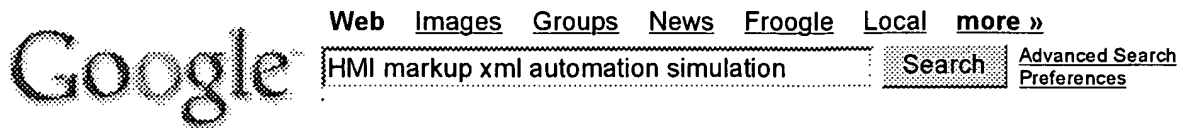
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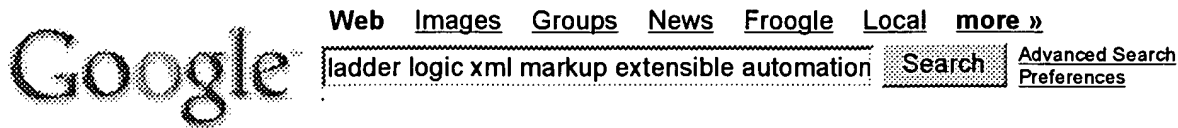
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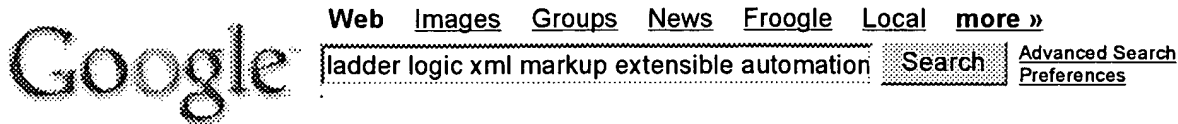
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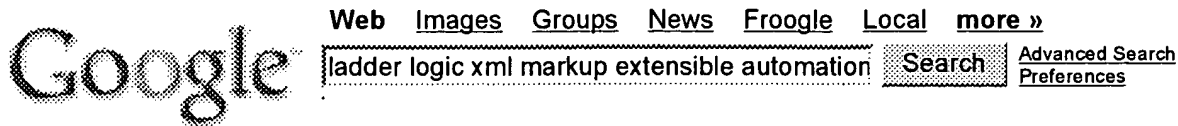
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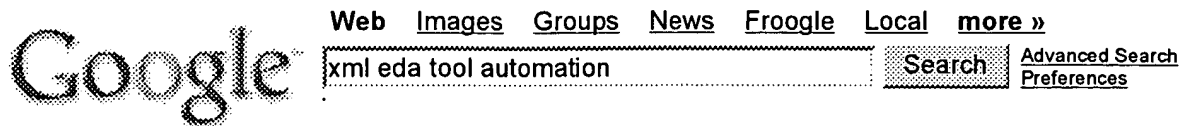
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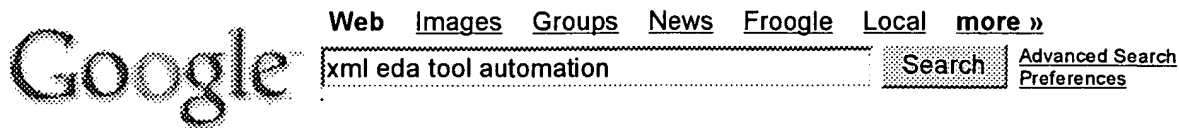


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assembly. The goal was to provide a management tool for making yield projections, resource

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mathematically sophisticated tool support for such XML-based representations. See the end of Section 5.4 required by systems, semiconductor and design tools companies, which enhance a language-based design

Regular Expressions. Sugar 2.0 1 [www.eda.org/vfv/hm/](#) 2[www.cl.cam.ac.uk/~mjc/Sugar/FinalCatBPaper.ps](#)[LPSAT: A Unified Approach to RTL Satisfiability - Zhihong Zeng Priyank \(2001\) \(Correct\) \(3 citations\)](#)

Can Greatly Impact The Performance Of Many EDA Tools. With Respect To Applications In Vlsi Cad, Most

applications in the electronic design automation (EDA) arena, including test pattern generation, timing

Can Greatly Impact The Performance Of Many EDA Tools. With Respect To Applications In Vlsi Cad,

[www.sigda.org/Archives/ProceedingArchives/Date/Date2001/papers/2001/date01/htmlfiles/sun_sgi/.../pdffiles/06c](#)[Accelerating Boolean Satisfiability through.. - Zhao, Malik.. \(2001\) \(Correct\) \(1 citation\)](#)

domains, especially in Electronic Design Automation (EDA) and Artificial Intelligence. In the EDA domain,

Automation (EDA) and Artificial Intelligence. In the EDA domain, SAT is embedded in many areas of design

[www.capsl.udel.edu/COMPILER/ISSS01/iss01/papers/2001/iss01/pdf/p244.pdf](#)[A Parallel Interior-Point Algorithm for Linear Programming.. - Andersen, Andersen \(1998\) \(Correct\) \(2 citations\)](#)

Linear programming (LP) is an important tool in many areas of science, but unfortunately LPs

University, DK-5230 Odense M, Denmark. E-mail: [eda@busieco.ou.dk](#). y CORE, Catholic University of

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ATPG Using Fast Justification and Propagation on the.. - Paul Tafertshofer.. (Correct)
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Retiming for DSM with Area-Delay Trade-Offs and Delay Constraints - Tabbara (1999) (Correct) (1 citation)
 becoming more important. System-level integration **tools** would have to solve complex optimization problems been a lot of debate about the challenges that the **EDA** industry will face in the DSM era. In this Complexity It is generally agreed, within the **EDA** community [1]that Deep SubMicron (DSM)
www-cad.eecs.berkeley.edu/HomePages/atabbara/publications/ms.pdf

Formulae and Applications of Interconnect Estimation - Considering Shield Insertion (Correct)
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The Associative-Skew Clock Routing Problem - Chen, Kahng, Qu, Zelikovsky (1999) (Correct) (1 citation)
 and bounded-skew formulations dominate current **EDA tools**, i.e.relative sink delays are constrained for skew problem is easier to address within current **EDA** frameworks than useful-skew (skew-scheduling) zero- and bounded-skew formulations dominate current **EDA tools**, i.e.relative sink delays are constrained
vlsicad.cs.ucla.edu/papers/conference/c101.ps

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 overview of activities, organizations and European **tool** efforts Author: C. Meersman, E2S n.v. ESA Contract
www.vlsivie.tuwien.ac.at/mike/VHDLReport.ps

Teaching Custom Integrated Circuit Design and Verification - Don Bouldin Adam (Correct)
 test circuitry that are compatible with the Cadence **tool**set and AMIS-0.6 micron process are described. circuits using electronic design automation (**EDA**) software. Each individual is assigned a project and a second window exercising the appropriate **EDA tool** needed for the design. These practices have
microsys6.engr.utk.edu/ece/mse03-bouldin.pdf

On Using Satisfiability-Based Pruning Techniques in.. - Vasco Manquinho Joao (2000) (Correct) (4 citations)
 Covering problems are widely used as a modeling **tool** in Electronic Design Automation (**EDA**)Recent as a modeling **tool** in Electronic Design Automation (**EDA**)Recent years have seen dramatic improvements in many applications in Electronic Design Automation (**EDA**)examples of which include logic and sequential
www.sigda.org/Archives/ProceedingArchives/Date/Date2000/papers/2000/date00/htmlfiles/sun_sgi/.../pdffiles/05c

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Component Selection and Matching for IP-Based Design - Ting Zhang Stanford (2001) (Correct) (3 citations)

The system is based on Extensible Markup Language (XML) specification both for IP libraries (a repository IP component selection: IP-based design flow needs tools to facilitate the process of selecting the is part of the JavaCAD framework, an Internet-based EDA tool aimed at providing IP protection in www.sigda.org/Archives/ProceedingArchives/Date/Date2001/papers/2001/date01/htmlfiles/sun_sgi/.../pdffiles/02a

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[An Architecture for a Secure Service Discovery Service - Czerwinski, Zhao, Hodes.. \(1999\) \(Correct\) \(128 citations\)](#)

and queries use the eXtensible Markup Language (XML) to encode such factors as cost, performance, between components. This paper presents the **design** of the SDS, focusing on both the architecture of Section 2 begins our discussion by describing the **design** concepts used in order to achieve our goals. The daedalus.cs.berkeley.edu/publications/sds-mobicom99.ps.gz

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[Systems Performance Measurement on PCI Pamette - Laurent Moll \(1997\) \(Correct\) \(8 citations\)](#)
 the PCI interface, and all can be reconfigured in **circuit**. This paper describes a collection of simple the reconfigurable logic is seen as a coprocessor **designed** to work in harmony with the host processor. an exception. Therefore a significant part of the **design** effort in PCI Pamette has gone into understanding ftp.digital.com/pub/DEC/SRC/publications/shand/fccm97.ps

[A Control-Theoretic Approach to Flow Control - Keshav \(1991\) \(Correct\) \(203 citations\)](#)
 behalf of each conversation (also called channel, **circuit** or virtual **circuit** we use 'conversation' a continuous-time system model. This is used to **design** a variant of the control law that is also technique (4) This is used as the basis for the **design** of a stable rate-based flow control scheme (5) simon.cs.cornell.edu/Info/People/skeshav/tenet/Keshav91a.ps

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[Pragmatic Issues in Handling Miscommunication: Observations of .. - Ronnie Smith \(1996\) \(Correct\) \(2 citations\)](#)
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Spectral Methods for Matrix Rigidity with Applications to.. - Satyanarayana Lokam (1995) (Correct) (1 citation)

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Issues In Distributed Control For Atm Networks - Jonathan Turner (1995) (Correct) (1 citation)

antecedents (e.g. the telephone network, virtual **circuit** data networks) focuses on specific protocols and of ATM networks and explores some of the key **design** issues through a series of examples. It is hoped 11, 12, 13, 15, 17, 21] Discussion of alternative **design** approaches and useful surveys are rare [20] This www.cs.wustl.edu/cs/techreports/1995/wucs-95-12.ps.Z

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Terabit Burst Switching - Jonathan Turner (1997) (Correct) (65 citations)

Device Arrays :12 3.2 GaAs **Circuit** Technology : :14 4 System **Design** Using WDM Links and Electronic Switching 15 4.1 :25 5 System **Design** Using WDM Links and Switching 26 5.1 Hybrid www.cs.wustl.edu/cs/techreports/1997/wucs-97-49.ps.Z

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